

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Moore (et al.)**
Serial No.: **10/801,942**
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Examiner: **Johnson, Brian P.**
Group Art Unit: **2183**

Title: **Computer Processor Array With Dedicated Interconnections (as amended)**

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No.: **40972**

TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT

Enclosed for filing in the above-referenced application are the following:

1. Information Disclosure Statement (citing 118 references);
2. Form PTO-1449 (citing 118 references);
3. 48 non-United States cited references; and
4. 5 European Search Reports to show relevance of non-English references.

Respectfully Submitted,

/January 7, 2009/
Date: _____

/Larry E. Henneman, Jr./

Larry E. Henneman, Jr., Reg. No. 41,063
Henneman & Associates, PLC
714 West Michigan Avenue
Three Rivers, MI 49093

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being filed with the United States Patent and Trademark Office on the date shown below via the Office's Electronic Filing System.

/January 7, 2009/
Date: _____

/Larry E. Henneman, Jr./

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